

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

 **ORIGINAL**

PATENT APPLICATION
ATTORNEY DOCKET NO. 200302263-2

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Luiz A. BARROSO et al.

Confirmation No.: 2813

Application No.: 10/698,130

Examiner: T. V. Nguyen

Filing Date: 10/31/2003

Group Art Unit: 2187

Title: MULTIPROCESSOR CACHE COHERENCE SYSTEM AND METHOD IN WHICH
PROCESSOR NODES AND INPUT/OUTPUT NODES ARE EQUAL PARTICIPANTS

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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